L Number	Hits	Search Text	DB	Time stamp
-	169	712/34.ccls.	USPAT; US-PGPUB	2003/05/02 14:21
-	275	712/32.ccls.	USPAT; US-PGPUB	2002/08/14 08:18
-	147	345/503.ccls.	USPAT; US-PGPUB	2002/08/12 15:07
-	3757	(co$1processor (dual adj1 processor)) and address and data and (burst single)	USPAT; US-PGPUB	2002/08/12 16:31
-	1574	(co$1processor (dual adj1 processor)) and address and data and (burst single) and memory and (control near5 register)	USPAT; US-PGPUB	2002/08/12 16:33
-	1245	(co$1processor (dual adj1 processor)) and address and (data near5 transfer) and (burst single) and memory and (control near5 register)	USPAT; US-PGPUB	2002/08/12 16:33
-	628	(co$1processor (dual adj1 processor)) and ((address and data) adj2 bus) and (data near5 transfer) and (burst single) and memory and (control near5 register)	USPAT; US-PGPUB	2002/08/13 08:20
-	1081	(data near5 transfer) same (burst single) and (co-processor coprocessor (dual near2 processor))	USPAT; US-PGPUB	2002/08/13 08:49
-	598	(data near5 transfer) same (burst single) and (co-processor coprocessor (dual near2 processor)) and (control near2 register)	USPAT; US-PGPUB	2002/08/13 08:50
-	567	(data near5 transfer) same (burst single) and (co-processor coprocessor (dual near2 processor)) and (control near2 register) and ((address data) near3 bus)	USPAT; US-PGPUB	2002/08/13 08:55
-	489	(data near5 transfer) same (burst single) and (co-processor coprocessor (dual adj2 processor)) and (control near2 register) and ((address data) near3 bus)	USPAT; US-PGPUB	2002/08/13 09:05
-	202	(data near5 transfer) same burst and (co-processor coprocessor (dual adj2 processor)) and (control near2 register) and ((address data) near3 bus)	USPAT; US-PGPUB	2002/08/13 10:29
-	234	712/225.ccls.	USPAT; US-PGPUB	2002/08/13 09:31
-	154	712/225.ccls. and coprocessor co-processor and burst and control adj1 register	USPAT; US-PGPUB	2002/08/13 09:35
-	55	712/31.ccls.	USPAT; US-PGPUB	2002/08/13 09:35
-	196	(data near2 transfer) same burst and (co-processor coprocessor (dual adj2 processor)) and (control near2 register) and ((address data) near3 bus)	USPAT; US-PGPUB	2002/08/13 10:31
-	337	(data near2 transfer) and coprocessor co-processor (dual adj1 processor)	EPO; JPO	2002/08/13 10:33
-	250	(data near2 transfer) and coprocessor co-processor (dual adj1 processor) and burst and (address near2 bus) and (data near2 bus)	EPO; JPO	2002/08/13 10:37
-	619	dual adj processor	USPAT; US-PGPUB	2002/08/13 10:38
-	224	dual adj processor and data adj transfer	USPAT; US-PGPUB	2002/08/13 10:39
-	40	dual adj processor and data adj transfer and (co-processor coprocessor)	USPAT; US-PGPUB	2002/08/13 10:39
-	22	712/34.ccls. and burst	USPAT; US-PGPUB	2002/08/13 15:48
-	22	712/34.ccls. and burst	USPAT; US-PGPUB	2002/08/13 15:13
-	1	6321310.pn.	USPAT; US-PGPUB	2002/08/13 15:48
-	124	(coprocessor co-processor) and (internal adj1 memory) and (control adj1 register) and burst	USPAT; US-PGPUB	2002/08/13 16:15
-	28	712/32.ccls. and burst	USPAT; US-PGPUB	2002/08/15 10:09

-	6	el-kik.in.	USPAT; US-PGPUB	2002/08/14 09:45
-	13	burst adj mode adj bit	USPAT; US-PGPUB	2002/08/14 11:32
-	16	712/35.ccls. and burst	USPAT; US-PGPUB	2002/08/15 11:10
-	28	712/32.ccls. and burst	USPAT; US-PGPUB	2002/08/15 11:10
-	246	burst and transfer same (coprocessor co-processor)	USPAT; US-PGPUB	2002/08/15 15:00
-	509	@ad<=20000106 and (burst with transfer) and ((master and slave) co-processor coprocessor) and (register with control) and (bit with (mode burst))	USPAT; US-PGPUB	2002/12/12 09:03
-	514	@ad<=20000106 and (burst with transfer) and ((master and slave) co-processor coprocessor) and (register with control) and ((bit flag) with (mode burst))	USPAT; US-PGPUB	2002/12/12 09:03
-	67	@ad<=20000106 and (burst with transfer) same ((master and slave) co-processor coprocessor) and (register with control) and ((bit flag) with (mode burst))	USPAT; US-PGPUB	2002/12/12 14:49
-	186	@ad<=20000106 and 710/35.ccls.	USPAT; US-PGPUB	2002/12/12 15:29
-	12	@ad<=20000106 and 710/35.ccls. and (coprocessor co-processor)	USPAT; US-PGPUB	2002/12/12 15:20
-	274	@ad<=20000106 and 710/33.ccls.	USPAT; US-PGPUB	2002/12/12 15:29
-	292	@ad<=20000106 and 710/110.ccls.	USPAT; US-PGPUB	2002/12/12 15:28
-	146	@ad<=20000106 and 710/35.ccls. and burst	USPAT; US-PGPUB	2002/12/12 15:29
-	66	@ad<=20000106 and 710/33.ccls. and burst	USPAT; US-PGPUB	2002/12/12 16:01
-	69	@ad<=20000106 and 710/110.ccls. and burst	USPAT; US-PGPUB	2002/12/12 15:29
-	30	@ad<=20000106 and burst with ((master and slave) coprocessor co-processor) and (increment with address) and (register with control) and ((mode burst) with bit)	USPAT; US-PGPUB	2002/12/12 16:03
-	1019	DMA same (start with address)	USPAT; US-PGPUB	2003/05/02 14:22
-	806	DMA same (start with address) and ((address and data) with bus)	USPAT; US-PGPUB	2003/05/02 15:25
-	1	5809309.pn.	USPAT; US-PGPUB	2003/05/02 14:27
-	123	(DMA with (coprocessor co-processor processor)) same (start with address) and ((address and data) with bus)	USPAT; US-PGPUB	2003/05/02 15:27
-	10	(DMA with (controllor coprocessor co-processor processor) same (internal with (memory RAM))) same (start with address) and ((address and data) with bus)	USPAT; US-PGPUB	2003/05/05 07:59
-	634	710/22.ccls.	USPAT; US-PGPUB	2003/05/02 16:04
-	149	710/26.ccls.	USPAT; US-PGPUB	2003/05/02 16:08
-	3	(coprocessor co-processor) same (internal adj (RAM memory)) same DMA and (control adj register)	USPAT; US-PGPUB	2003/05/02 16:10
-	8	(coprocessor co-processor) same (internal adj (RAM memory)) same DMA	USPAT; US-PGPUB	2003/05/02 16:11
-	6	("4263650" | "4292668" | "4300193" | "4378591" | "4514808" | "4530053").PN.	USPAT	2003/05/02 16:12
-	23	4631671.URPN.	USPAT	2003/05/02 16:14
-	14	("4031517" | "4279016" | "4414624" | "4439839" | "4577282" | "4646232" | "4669043" | "4713748" | "4829475" | "4878190" | "4912636" | "4930068" | "5077657" | "5560019").PN.	USPAT	2003/05/02 16:18
-	1	5809309.URPN.	USPAT	2003/05/02 16:20

–	333	(DMA with (controllor coprocessor co-processor processor) same (internal with (memory RAM)))	USPAT; US-PGPUB	2003/05/05 07:59
–	93	(DMA near2 (controllor coprocessor co-processor processor) same (internal with (memory RAM)))	USPAT; US-PGPUB	2003/05/05 08:00
–	66	(DMA near2 (controllor coprocessor co-processor processor) same (internal near3 (memory RAM)))	USPAT; US-PGPUB	2003/05/05 08:00
–	68	709/211.ccls.	USPAT; US-PGPUB	2003/05/05 17:01
–	9	709/211.ccls. and DMA	USPAT; US-PGPUB	2003/05/05 17:01
–	12	("4030074" | "4208650" | "4298928" | "4326291" | "4351023" | "4368514" | "4369443" | "4424565" | "4456933" | "4466098" | "4471427" | "4481578").PN.	USPAT	2003/05/05 17:06
–	1	"4099236".PN.	USPAT	2003/05/05 17:07
–	82	4700292.URPN.	USPAT	2003/05/05 17:10
–	19	4700292.URPN. and DMA	USPAT	2003/05/05 17:15
–	191	709/212.ccls.	USPAT	2003/05/06 13:21
–	236	709/216.ccls.	USPAT	2003/05/06 14:17
–	25	transputer and INMOS.as.	USPAT	2003/05/06 07:51
–	14	transputer and INMOS.as. and transfer	USPAT	2003/05/06 07:52
–	0	transputer and INMOS.as. and DMA	USPAT	2003/05/06 07:52
–	23	transputer and INMOS.as. and array	USPAT	2003/05/06 07:56
–	104	INMOS.as.	USPAT	2003/05/06 07:57
–	5	INMOS.as. and 712/$3.ccls.	USPAT	2003/05/06 07:59
–	907	712/10-22.ccls.	USPAT	2003/05/06 08:02
–	136	712/10-22.ccls. and DMA	USPAT	2003/05/06 08:04
–	630	712/10-22.ccls. and transfer	USPAT	2003/05/06 08:05
–	56	712/10-22.ccls. and transfer with DMA	USPAT	2003/05/06 14:45
–	192	709/216.ccls. and (transfer$3 DMA)	USPAT	2003/05/06 14:17
–	142	710/34.ccls.	USPAT	2003/05/06 14:45